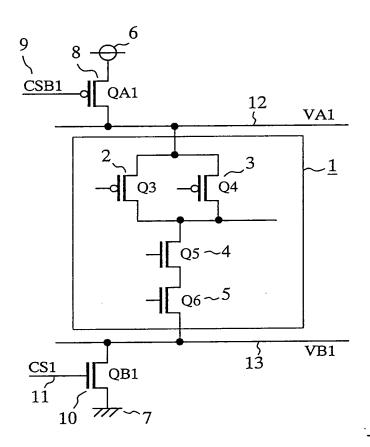
TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

INVENTOR(S): HIROSHI MAKINO

APPLICATION SERIAL NO: 027260-669 SHEET 1 of 8

FIG.1

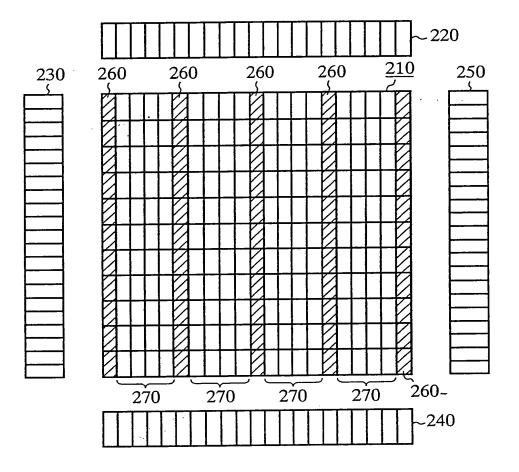


TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**INVENTOR(S):** HIROSHI MAKINO

APPLICATION SERIAL NO: 027260-669 SHEET 2 of 8

FIG.2(PRIOR ART)



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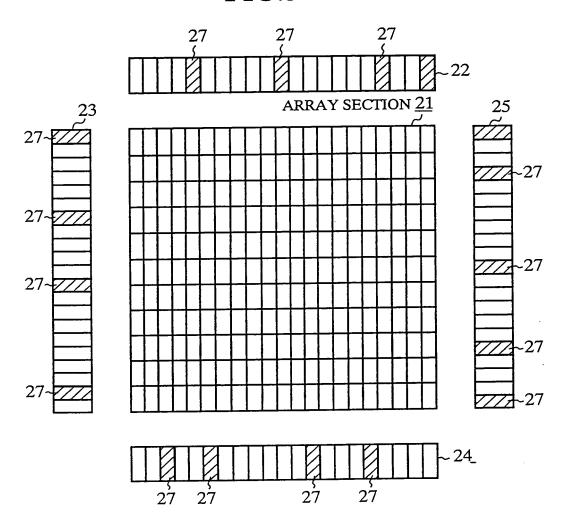
TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**INVENTOR(S):** HIROSHI MAKINO

APPLICATION SERIAL NO: 027260-669

SHEET 3 of 8

FIG.3



TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**INVENTOR(S):** HIROSHI MAKINO

APPLICATION SERIAL NO: 027260-669 SHEET 4 of 8

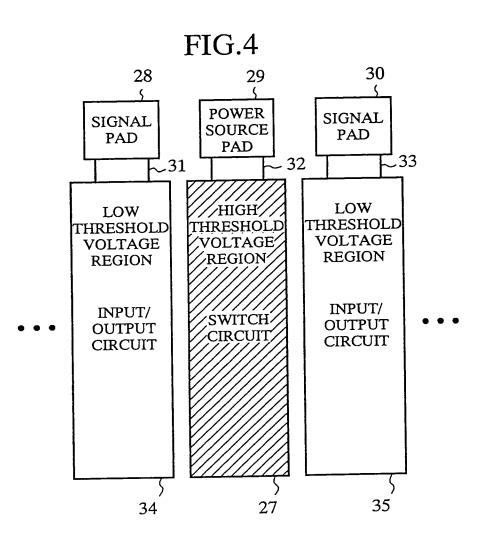
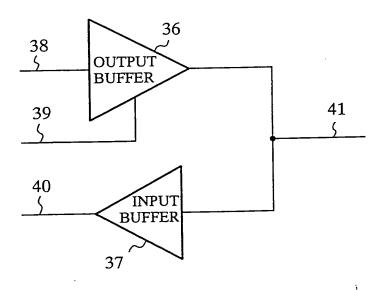


FIG.5



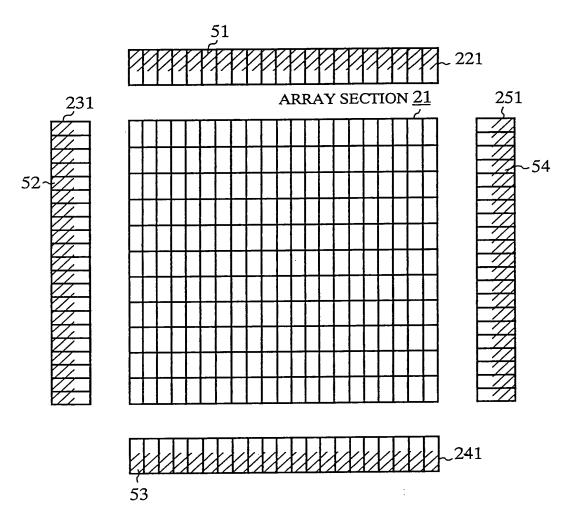
APPLN. FILING DATE: AUGUST 4, 2003

TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

INVENTOR(S): HIROSHI MAKINO

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FIG.6

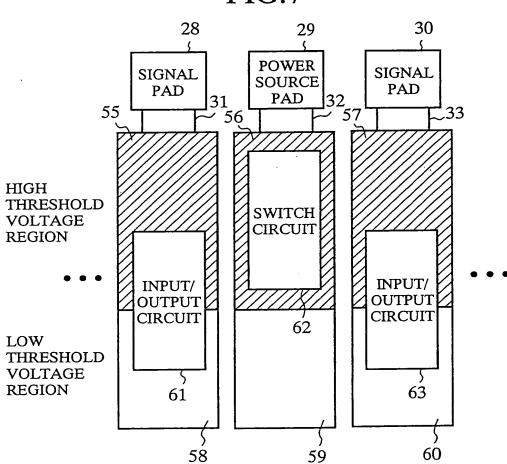


TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**INVENTOR(S):** HIROSHI MAKINO

APPLICATION SERIAL NO: 027260-669 SHEET 6 of 8



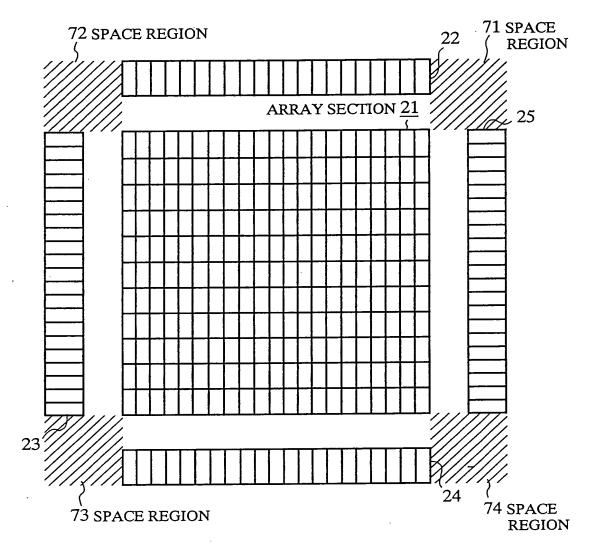


TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**INVENTOR(S):** HIROSHI MAKINO

APPLICATION SERIAL NO: 027260-669 SHEET 7 of 8

FIG.8



TITLE: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**INVENTOR(S):** HIROSHI MAKINO

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FIG.9

